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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Serial No.: JOSEPH T. EVANS, JR. ET AL. 582,672
Filed: September 14, 1990 GROUP 280
Group: 233
For: NON-VOLATILE MEMORY CIRCUIT USING
FERROELECTRIC CAPACITOR STORAGE ELEMENT

Honorable Commissioner of
Patents and Trademarks
Washington, D.C. 20231

Dear Sir:

ASSOCIATE POWER OF ATTORNEY

I, Dale B. Nixon, Registration No. 28,454, hereby
appoint:

Lee Patch, Registration No. 30,095

Michael A. Glenn, Registration No. 30,176

Gail W. Woodward, Registration No. 22,645

whose address is National Semiconductor Corporation,
P. O. Box 58090, Santa Clara, California 95052-8090, as
associate attorneys in the above-identified application for
Letters Patent, with full power to transact all business in
the Patent and Trademark Office connected therewith.

Respectfully submitted,

RICHARDS, MEDLOCK & ANDREWS

By Dale B. Nixon
Dale B. Nixon
Registration No. 28,454

DBN:mw
October 9, 1990
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I hereby certify that this correspondence is being deposited
with the United States Postal Service as first class mail
in an envelope addressed to:
Commissioner of Patents and Trademarks,

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(Date of Deposit)

Dale B. Nixon, Reg. No. 28,454

Name of applicant, assignee, or
Registered Representative

Dale B. Nixon

Signature

10/9/90

Date of Signature